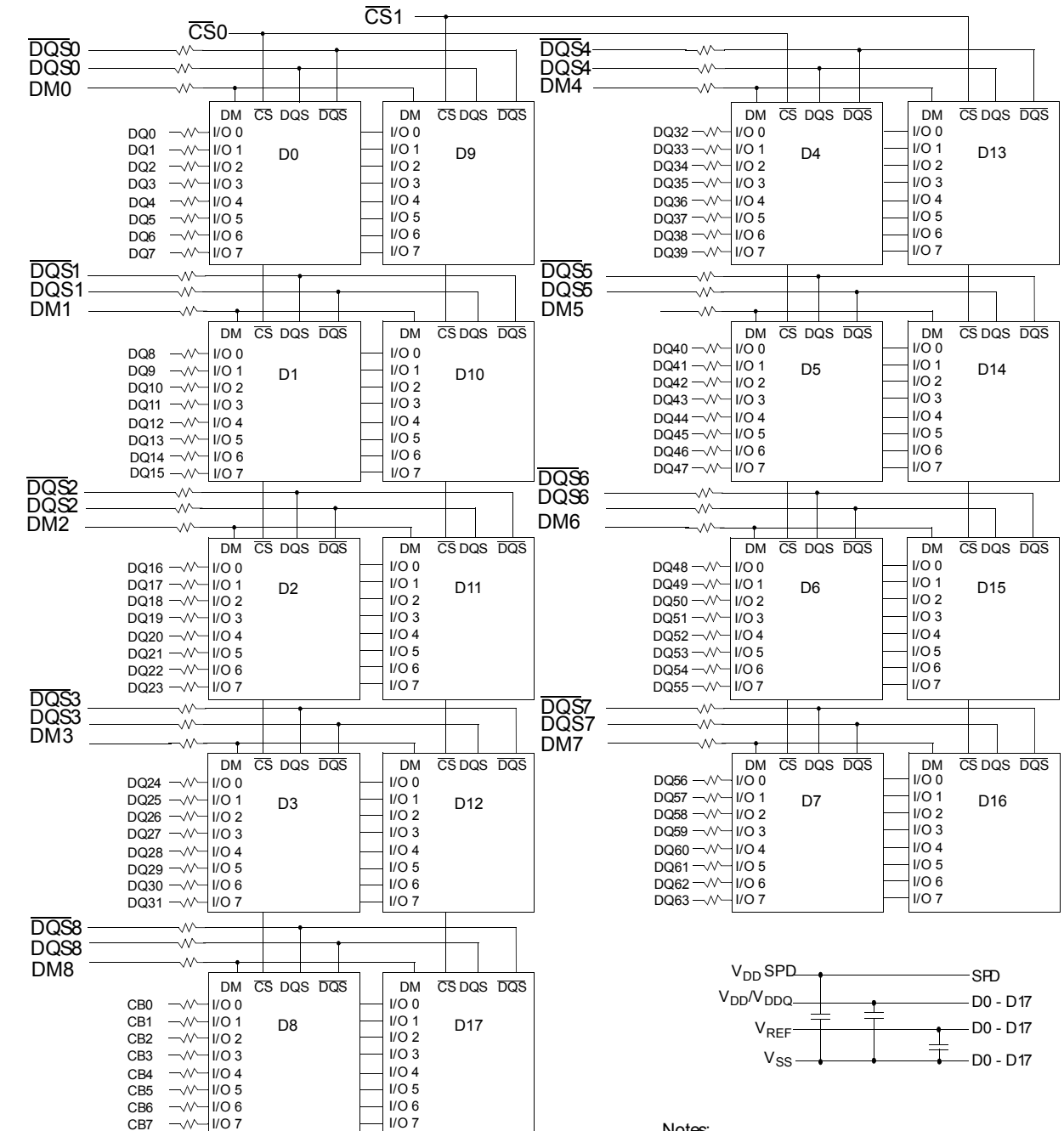
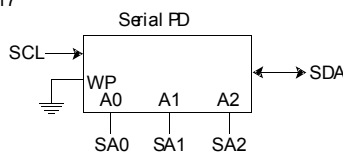


Functional Block Diagram



- BA0 - BA2 → BA0-BA2: SDRAMs D0 - D17
- A0 - AN → A0 - AN: SDRAMs D0 - D17
- CKE0 → CKE: SDRAMs D0 - D8
- CKE1 → CKE: SDRAMs D9 - D17
- \overline{RAS} → \overline{RAS} : SDRAMs D0 - D17
- \overline{CAS} → \overline{CAS} : SDRAMs D0 - D17
- WE → WE: SDRAMs D0 - D17
- ODT0 → ODT: SDRAMs D0 - D8
- ODT1 → ODT: SDRAMs D9 - D17



- Notes:
1. DQ-to-I/O wiring is shown as recommended but may be changed.
 2. DQ/DQS/DQS/ODT/DM/CKE/ \overline{CS} relationships must be maintained as shown.
 3. DQ, CB, DQS, DM/DQS/DQS resistors: Refer to associate topology diagram
 4. BAx, Ax, RAS, CAS, WE resistors: Refer to associate topology diagram.